

Master 2 Project: Integration of $\text{Bi}_{1-x}\text{Sb}_x$ materials on Silicon **First principle calculations**

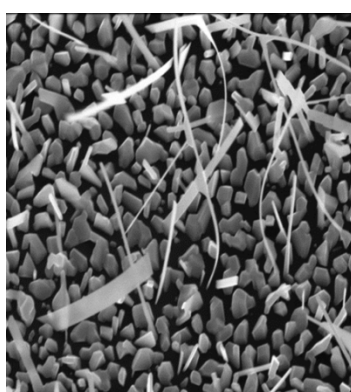
Supervisors:

Anne Hemeryck (M3)
Sébastien Plissard (MPN)

Thermoelectric devices are made from materials that can convert a temperature difference into electricity, making them an ideal too harvest heat losses in any device. The phenomenon is reversible: if electricity is applied to a thermoelectric device, it can produce a temperature difference. Today, thermoelectric devices are used for relatively low-power applications, such as powering small sensors along oil pipelines, backing up batteries on space probes, and cooling minifridges.

Among thermoelectric materials, bismuth antimonides ($\text{Bi}_{1-x}\text{Sb}_x$) are used as the n-type legs in many thermoelectric devices below room temperature. The thermoelectric efficiency, given by its figure of merit zT , peaks for compositions x around 10-15%, and belongs to the highest ever reported¹. Considering a nanowire geometry, the zT coefficient can be improved further by reducing phonon conduction (due to lateral confinement) and keeping the electrical one unchanged.

Here, we propose to study the direct integration of BiSb nanowires on silicon by first principle calculations. This project is a collaboration between MPN and M3. Early results on BiSb integration on Si(001) are already reported (M2 project of Wouter de Jong, see figure 1). We propose to extend this study to Si(110) and Si(111) surfaces as they represent native growth surfaces for BiSb nanowires.



30° Tilted SEM images

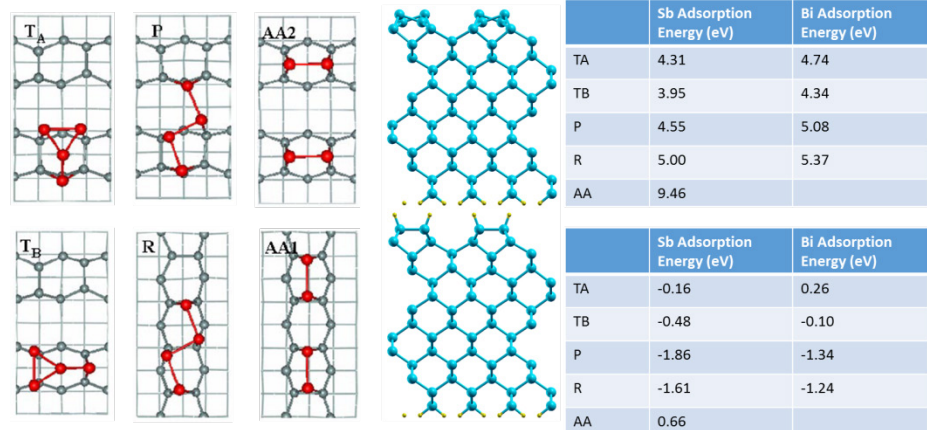


Figure 1: On left, the direct integration of BiSb nanowires on Si(001).
On Right, the first principle modeling of Sb and Bi incorporation on a Si(001) surface.

References:

- [1] Smith, G. E.; Wolfe, R. (1962-03-01). "Thermoelectric Properties of Bismuth-Antimony Alloys". *Journal of Applied Physics*. 33 (3): 841–846. doi:10.1063/1.1777178. ISSN 0021-8979.